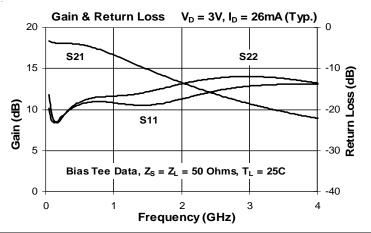


Product Description

Sirenza Microdevices' SGC-2363Z is a high performance SiGe HBT MMIC amplifier utilizing a Darlington configuration with a patented active-bias network. The active bias network provides stable current over temperature and process Beta variations. Designed to run directly from a 3V supply, the SGC-2363Z does not require a dropping resistor as compared to typical Darlington amplifiers. The SGC-2363Z is designed for high linearity 3V gain block applications that require small size and minimal external components. It is internally matched to 50 ohms.

The matte tin finish on Sirenza's lead-free "Z" package is applied using a post annealing process to mitigate tin whisker formation and is RoHS compliant per EU Directive 2002/95. The package body is manufactured with green molding compounds that contain no antimony trioxide or halogenated fire retardants.



Preliminary Information

SGC-2363Z



50-4000 MHz Silicon Germanium Cascadable Gain Block



Product Features

- Single Fixed 3V Supply
- Supply Dropping Resistor not required
- Patented Self-Bias Circuitry
- P1dB = 9.8 dBm at 1950 MHz
- IP3 = 23.5 dBm at 1950 MHz
- Robust 1000V ESD, Class 1C HBM

Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

Symbol	Parameters	Units	Frequency	Min.	Тур.	Max.
			850 MHz		17.4	
G	Small Signal Gain	dB	1950 MHz		13.6	
			2400 MHz		12.3	
			850 MHz		10.4	
P _{1dB}	Output Power at 1dB Compression	dBm	1950 MHz		9.8	
			2400 MHz		9.6	
			850 MHz		22.9	
OIP ₃	Output Third Order Intercept Point	dBm	1950 MHz		23.5	
			2400 MHz		23.8	
IRL	Input Return Loss	dB	1950 MHz		17.9	
ORL	Output Return Loss	dB	1950 MHz		14.3	
NF	Noise Figure	dB	1930 MHz		3.7	
V_D	Device Operating Voltage	V			3	
I _D	Device Operating Current	mA		22	26	30
Rth, j-l	Thermal Resistance (junction to lead)	°C/W			255	

Test Conditions:

 $V_{\rm D} = 3.0 V$

 $I_D = 26mA Typ$.

 $T_1 = 25^{\circ}C$

OIP₃ Tone Spacing = 1MHz

Bias Tee Data $Z_S = Z_L = 50 \text{ Ohms}$

Pout per tone = -5 dBm

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Typical RF Performance at Key Operating Frequencies (Bias Tee)								
Symbol	Parameter		Unit Frequency (MHz)					
	r ai ailletei	Oilit	100	500	850	1950	2400	3500
G	Small Signal Gain	dB	18.4	18.1	17.4	13.6	12.3	9.8
OIP ₃	Output Third Order Intercept Point	dBm		23.3	22.9	23.5	23.8	22.1
P _{1dB}	Output Power at 1dB Compression	dBm		11.0	10.4	9.8	9.6	8.3
IRL	Input Return Loss	dB	23.6	19.0	18.0	17.9	16.3	14.2
ORL	Output Return Loss	dB	22.4	18.4	16.7	14.3	13.0	12.5
S ₁₂	Reverse Isolation	dB	20.5	21.6	22.2	20.5	19.9	18.9
NF	Noise Figure	dB	2.9	3.0	3.3	3.7	3.9	4.7

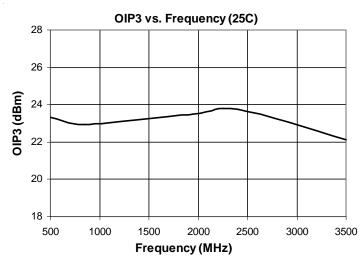
Test Conditions: $V_D = 3V$ $T_L = 25$ °C

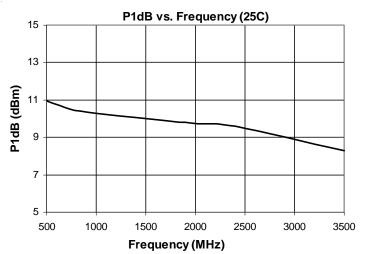
 $I_D = 26mA Typ.$

OIP₃ Tone Spacing = 1MHz, Pout per tone = -5 dBm

 $Z_S = Z_L = 50 \text{ Ohms}$

Typical Performance with Bias Tee, $V_D = 3V$, $I_D = 26mA$ (Typ.)





Absolute Maximum Ratings				
Parameter	Absolute Limit			
Max Device Current (I _{CE})	55 mA			
Max Device Voltage (V _{CE})	4.5 V			
Max. RF Input Power* (See Note)	+18 dBm			
Max. Junction Temp. (T _J)	+150°C			
Operating Temp. Range (T _L)	-40°C to +85°C			
Max. Storage Temp.	+150°C			

*Note: Load condition, $Z_L = 50$ Ohms

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one.

Bias Conditions should also satisfy the following expression:

 $I_DV_D < (T_J - \overline{T_L}) / R_{TH}, j-I$ $T_L = T_{LEAD}$

Reliability & Qualification Information	n
Parameter	Rating
ESD Rating - Human Body Model (HBM)	Class 1C
Moisture Sensitivity Level	MSL 1

This product qualification report can be downloaded at www.sirenza.com



Caution: ESD sensitive

Appropriate precautions in handling, packaging and testing devices must be observed.

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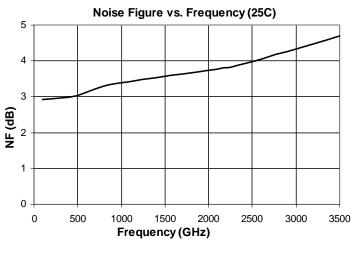
Broomfield, CO 80021

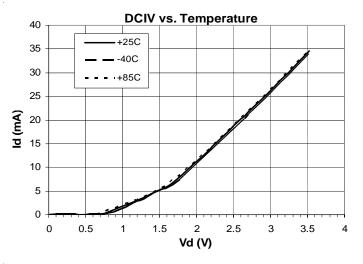
Phone: (800) SMI-MMIC

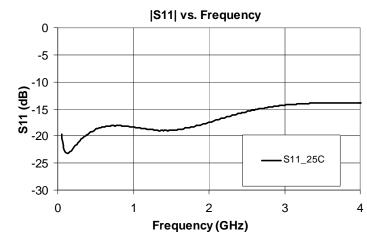
EDS-104973 Rev A

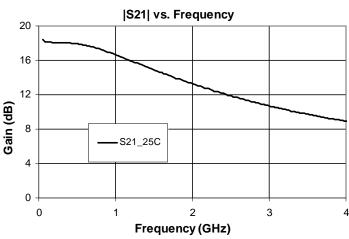


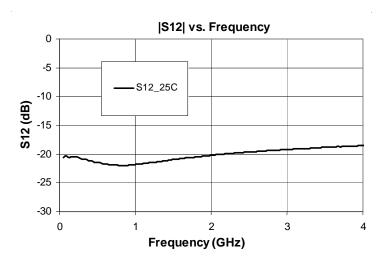
Typical Performance with Bias Tee, $V_D = 3V$, $I_D = 26mA$ (Typ.)

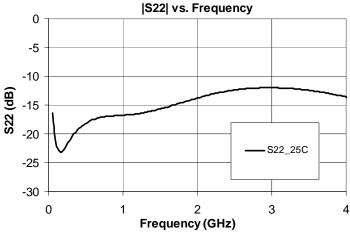








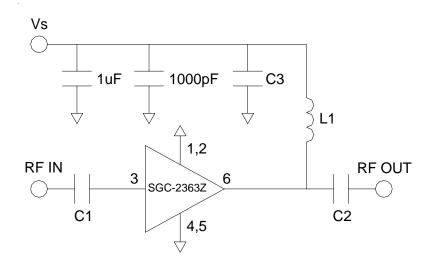


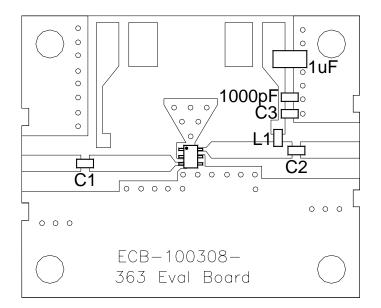


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Pin #	Function	Description
3	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation
1,2,4,5	GND	Connection to ground. Use via holes as close to the device ground leads as possible to reduce ground inductance and achieve optimum RF performance
6	RF OUT / DC BIAS	RF output and bias pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.

Application Circuit Schematic

Application Circuit Element Values					
Reference Designator	100-2000MHz	2000-4000MHz			
C1	1000pF	2.7pF			
C2	100pF	6.8pF			
С3	100pF	6.8pF			
L1	150nH	39nH			

Mounting Instructions

- 1. Use a large ground pad area under device pins 1, 2, 4 and 5 with many plated through-holes as shown.
- 2. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.

Part Identification Marking & Pinout

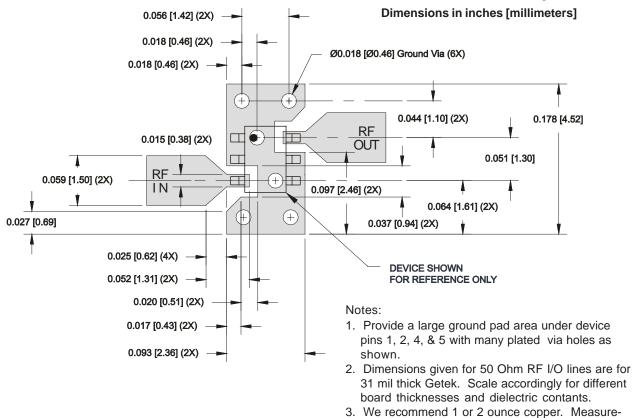


Part Package / Number Lead Composition		Reel Size	Devices / Reel	
SGC-2363Z	Lead Free, RoHs Compliant	7"	3000	



SOT-363 PCB Pad Layout

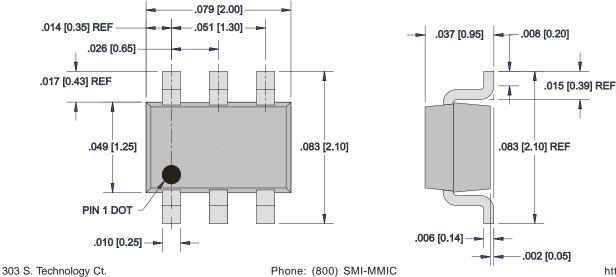
ments for this data sheet were made on a 31 mil thick Getek with 1 ounce copper on both sides.



SOT-363 Nominal Package Dimensions

Dimensions in inches [millimeters]

A link to the SOT-363 package outline drawing with full dimensions and tolerances may be found on the product web page at www.sirenza.com.



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